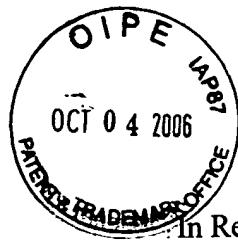


JFW

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of

Nakamura

Serial No.: 10/809,118

Filed: March 25, 2004

For: Semiconductor Device And
Manufacturing Method Thereof

Art Unit:

Examiner: Unknown

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to:
Commissioner for Patents ,
P.O. Box 1450, Alexandria, VA 22313-1450 on
<u>October 2, 2006</u>
(Date of Deposit)
Shannon Wallace
Name of applicant, assignee, or Registered Rep.
<u>Shannon Wallace 10/2/06</u>
Signature
Date

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

STATUS INQUIRY

Gentlemen:

Please advise as to the status of the above-identified patent application.

More than two (2) years have passed since Applicants filed this application.

Accordingly, the Applicants respectfully requests the current status of the above-identified patent application.

Respectfully submitted,

Dated: October 2, 2006

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